NASA-DoD Lead-Free Electronics Project

June 24, 2009 Tin Whisker Group Telecon

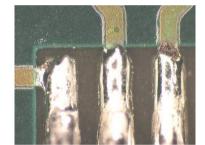






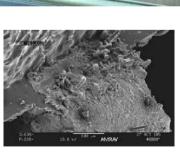




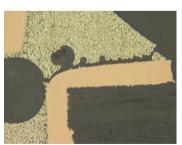












BAE SYSTEMS

Rockwell Collins

Honeywell

ASCORPIO Solutions

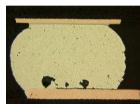
Raytheon

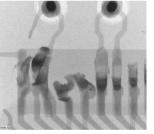
COM DEV

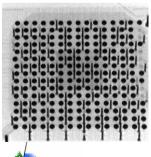
LOCKHEED MARTIN











Tremier











Testing project will build on the results from the JCAA/JGPP LFS Project

The primary technical objective of this project is to undertake comprehensive testing to generate information on failure modes/criteria to better understand the reliability of:

Packages (e.g., Thin Small Outline Package [TSOP], Ball Grid Array [BGA], Plastic Dual In-line Package [PDIP]) assembled and reworked with lead-free alloys

Packages (e.g., TSOP, BGA, PDIP) assembled and reworked with mixed (lead/lead-free) alloys.

Web Links:

NASA-DoD Lead-Free Electronics Project:

http://www.teerm.nasa.gov/projects/NASA_DODLeadFreeElectronics_Proj2.html

JCAA/JGPP Lead-Free Solder Project

http://www.teerm.nasa.gov/projects/LeadFreeSolderTestingForHighReliability_Proj1.html

Comparison of NASA-DoD LFE Project to predecessor JCAA/JG-PP LFS Project

Similarities

- Virtually identical test vehicle
- Procedures identical for most tests
- Same facility for assembly
- SN100C being used for wave soldering

Differences

- Test articles will be thermally aged after assembly
- Increased rework
- Increased solder mixing
- Mechanical shock test procedure
- Drop testing
- Immersion Ag surface finish for most test vehicles
 - Limited number will have ENIG
- SAC305 being used for reflow soldering
- SN100C being used for reflow soldering

Rockwell Collins

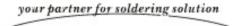


BAE SYSTEMS

























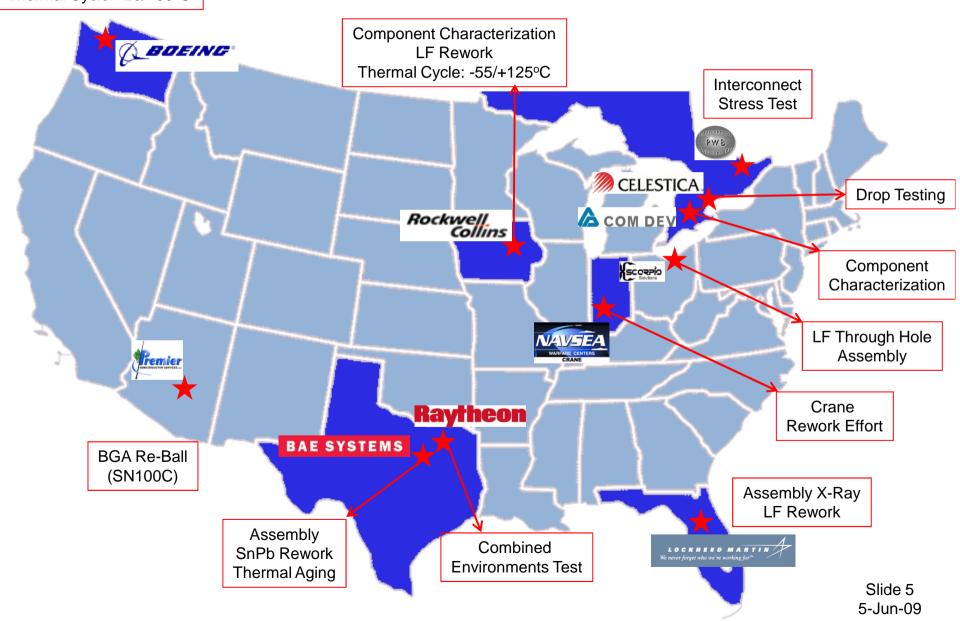




TEXAS INSTRUMENTS

Slide 4 5-Jun-09 Vibration Mechanical Shock Thermal Cycle: -20/+80°C

NASA-DoD Lead-Free Electronics Project Stakeholder Locations



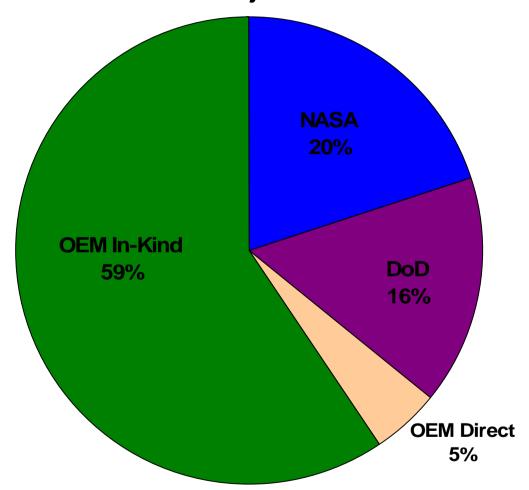
Joint Test Protocol Endorsement

Endorsement signifies agreement that the JTP contains performance and technical requirements applicable to specific applications within programs, and provides the consensus needed to move forward with testing.

- AIA (Aerospace Industries Association)
- Air Force Electronic Engineer (WR-ALC/ENFM)
- Air Force Director of Engineering (DOE) for the 312/326 Aeronautical Systems Wing (AESW); Wright-Patterson Air Force Base
- Army Research Lab
- Headquarters Air Force Space Command
- NASA NEPP Program
- NASA-MSFC Packaging, EEE Parts & Electrical Manufacturing Branch Chief
- Naval Air Warfare Center, Aircraft Division
- MDA PMP Program Lead
- NSWC Crane Division 2M Project Manager
- NSWC Crane Division 2M (Miniature/Microminiature) Electronics Technician
- NSWC Crane Division Electronics Engineer, Testing:
 Printed Circuit Technologies Branch
- NSWC Crane Division Materials Engineer; FA/MA Branch, Flight Systems Division

- BAE Systems Principal Process Engineer
- BAE Systems Vice President of Engineering for Electronics and Integrated Solutions
- Celestica Director of Technology IAD sector
- COM DEV Director, Design Integrity
- General Dynamics Design Assurance Engineering Manager
- Harris Process Engineering Group Lead
- Lockheed Martin Engineering Manager
- Nihon Superior President of Nihon Superior
- Radiance Technologies, Inc. AERI Program Manager
- Rockwell Collins Director, Advanced Manufacturing Technology
- TT Apsco Vice President and General Manager
- Willcor Inc. Best Manufacturing Practices

Contributions to the NASA-DoD Lead-Free Electronics Project ~\$1.8 Million



Lead-Free Solder Alloys

- SAC305 (Sn3.0Ag0.5Cu)
 - Surface mount assembly

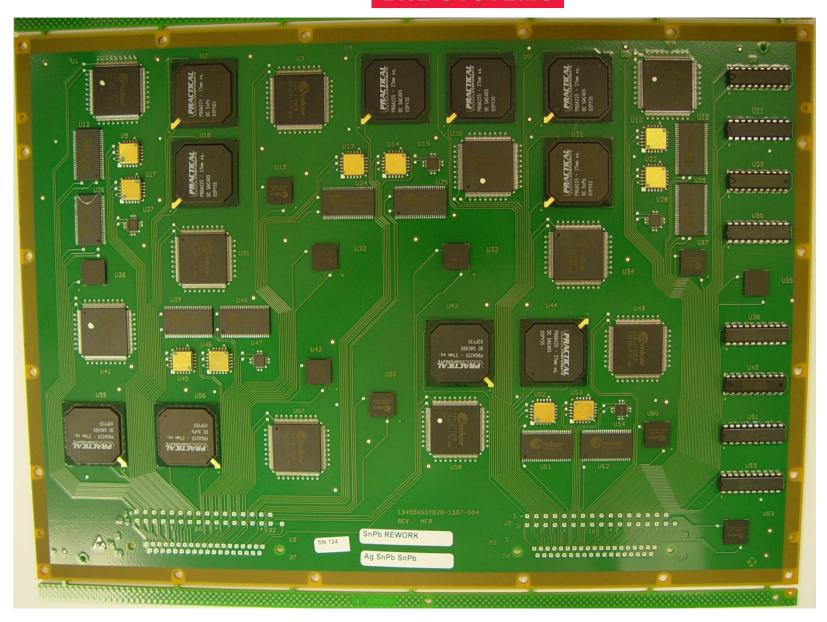
This alloy was chosen for reflow soldering because this particular solder alloy has shown the most promise as a primary replacement for tin-lead solder. The team decided that they wanted to select at least one "general purpose" alloy to be evaluated and it was determined that the SnAgCu solder alloy would best serve this purpose.

- SN100C (Sn0.7Cu0.05Ni+Ge)
 - Plated through hole
 - Surface mount assembly

This alloy is commercially available and the general trend in industry has been switching to the nickel stabilized tin-copper alloy over standard tin-copper due to superior performance. In addition, this nickel-stabilized alloy does not require special solder pots and has shown no joint failures in specimens with over 4 years of service.

193 Test Vehicles Assembled

120 = Manufactured 73 = Rework



Component Finish/Solder Combinations Example

	SnPb Manufactured Test Vehicles										
Component	Component Finish	Reflow Solder	Wave Solder	Board Finish							
BGA-225	SAC405	SnPb									
BGA-225	SnPb	SnPb									
CLCC-20	SAC305	SnPb									
CLCC-20	SnPb	SnPb									
CSP-100	SAC105	SnPb									
CSP-100	SnPb	SnPb		T							
PDIP-20	NiPdAu		SnPb	Immersion Silver							
PDIP-20	Sn		SnPb	Silvei							
QFN	Matte Sn	SnPb									
TQFP-144	Matte Sn	SnPb									
TQFP-144	SnPb Dip	SnPb									
TSOP-50	SnBi	SnPb									
TSOP-50	SnPb	SnPb									

Profiles used during initial assembly

Reflow Profile = SnPb

Preheat = ~ 120 seconds @140-183°C Solder joint peak temperature = 225°C Time above reflow = 60-90 sec Ramp Rate = 2-3 °C/sec

Wave Profile = SnPb

Solder Pot Temperature = 250°C Preheat Board T = 101°C Peak Temperature = 144°C Speed: 110 cm/min

Slide 10 5-Jun-09

Component Finish/Solder Combinations Example

	Lead-Free Manufactured Test Vehicles										
Component	Component Set A				Set B						
Component	Finish	Reflow Solder	Wave Solder	Board Finish	Reflow Solder	Wave Solder	Board Finish				
BGA-225	SnPb	SAC305			SN100C						
BGA-225	SAC405	SAC305			SN100C						
CLCC-20	SnPb	SAC305			SN100C						
CLCC-20	SAC305	SAC305		Immersion	SN100C						
CSP-100	SnPb	SAC305		Silver	SN100C						
CSP-100	SAC105	SAC305			SN100C		.				
PDIP-20	NiPdAu		SN100C	A limited		SN100C	Immersion				
PDIP-20	Sn		SN100C	Number of		SN100C	Silver				
QFN	Matte Sn	SAC305		Boards will be	SN100C						
TQFP-144	SnPb Dip	SAC305		Built with ENIG	SN100C						
TQFP-144	Matte Sn	SAC305			SN100C						
TSOP-50	SnPb	SAC305			SN100C						
TSOP-50	SnBi	SAC305			SN100C						

Profiles used during initial assembly

Reflow Profile = SAC305

Preheat = 60-120 seconds @150-190°C Peak temperature target = 243°C Reflow:~20 seconds above 230°C ~30-90 seconds above 220°C

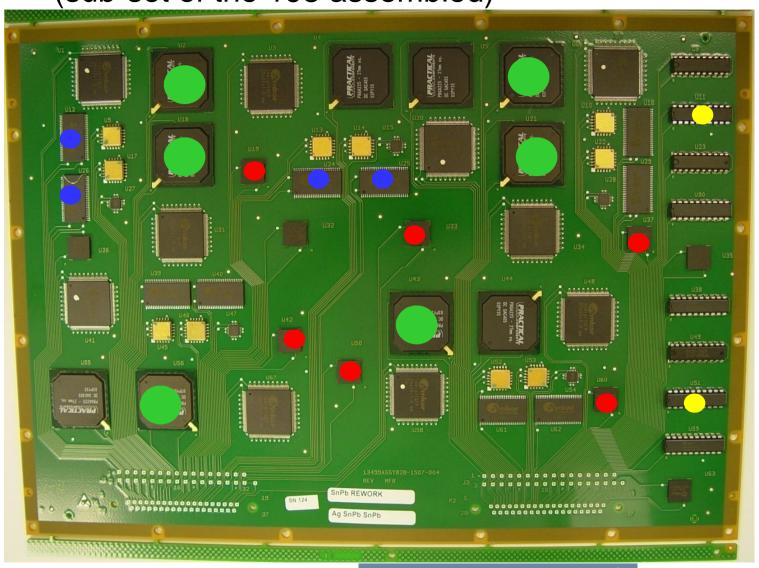
Wave Profile = SN100C

Solder Pot Temperature = 265°C Preheat Board T = 134°C Peak Temperature = 157°C Speed: 90 cm/min

Slide 11 5-Jun-09

73 Test Vehicles Being Reworked (sub-set of the 193 assembled)

RefDes	Component
U18	BGA-225
U43	BGA-225
U06	BGA-225
U02	BGA-225
U21	BGA-225
U56	BGA-225
U33	CSP-100
U50	CSP-100
U19	CSP-100
U37	CSP-100
U42	CSP-100
U60	CSP-100
U11	PDIP-20
U51	PDIP-20
U12	TSOP-50
U25	TSOP-50
U24	TSOP-50
U26	TSOP-50





BAE SYSTEMS



Component Finish/Solder Combinations Example

		SnPb F	Rework Test	Vehicles		
Component	Original Component Finish	Reflow Solder	Wave Solder	New Component Finish	Rework Solder	Board Finish
BGA-225	SAC405	SnPb				
BGA-225	SnPb	SnPb		SAC405	SnPb	
BGA-225	SnPb	SnPb		SnPb	Flux Only	
CLCC-20	SAC305	SnPb				
CSP-100	SAC105	SnPb				·
CSP-100	SnPb	SnPb		SnPb	Flux Only	Immersion
CSP-100	SnPb	SnPb		SAC105	SnPb	Silver
PDIP-20	NiPdAu		SnPb			A limited
PDIP-20	Sn		SnPb			Number of
PDIP-20	SnPb		SnPb	Sn	SnPb	Boards will be
QFN	Matte Sn	SnPb				Built with
TQFP-144	NiPdAu	SnPb				ENIG
TQFP-144	SnPb Dip	SnPb				
TSOP-50	Sn	SnPb				
TSOP-50	SnBi	SnPb				
TSOP-50	SnPb	SnPb		SnPb	SnPb	
TSOP-50	SnPb	SnPb		Sn	SnPb	

Profiles used during initial assembly

Reflow Profile = SAC305

Preheat = 60-120 seconds @150-190°C Peak temperature target = 243°C

Reflow:~20 seconds above 230°C

~30-90 seconds above 220°C

Wave Profile = SN100C

Solder Pot Temperature = 265°C

Preheat Board T = 134°C

Peak Temperature = 157°C

Speed: 90 cm/min

Slide 13 5-Jun-09

Component Finish/Solder Combinations Example

		Lead-Free R	Rework Test	Vehicles		
Component	Component Finish	Reflow Solder	Wave Solder	New Component Finish	Rework Solder	Board Finish
BGA-225	SnPb	SAC305				
BGA-225	SAC405	SAC305		SAC405	SnPb	
BGA-225	SAC405	SAC305		SAC405	Flux Only	
CLCC-20	SnPb	SAC305				
CSP-100	SnPb	SAC305				
CSP-100	SAC405	SAC305				
CSP-100	SAC105	SAC305		SAC105	Flux Only	
CSP-100	SAC105	SAC305		SAC105	SnPb	
PDIP-20	Sn		SN100C			Immersion Silver
PDIP-20	Sn		SN100C	Sn	SN100C	Silver
QFN	SnPb	SAC305				
TQFP-144	NiPdAu	SAC305				
TQFP-144	SAC 305 Dip	SAC305				
TSOP-50	SnBi	SAC305				
TSOP-50	SnPb	SAC305				
TSOP-50	Sn	SAC305		Sn	SnPb	
TSOP-50	SnBi	SAC305		SnBi	SAC305	

Profiles used during initial assembly

Reflow Profile = SAC305

Preheat = 60-120 seconds @150-190°C Peak temperature target = 243°C

Reflow:~20 seconds above 230°C

~30-90 seconds above 220°C

Wave Profile = SN100C

Solder Pot Temperature = 265°C

Preheat Board T = 134°C

Peak Temperature = 157°C

Speed: 90 cm/min

Slide 14 5-Jun-09

Testing Activities

Specific testing details can be found in the Joint Test Protocol (JTP) http://www.teerm.nasa.gov/reports.html

Thermal Cycling:
-20°C to +80°C

Rockwell
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Thermal Cycling:
-55°C to +125°C

Rockwell
Collins

Interconnect Stress
Testing

Combined

Combined

Combined
Environments Testing
Raytheon

Copper Dissolution





NAVSEA Crane Rework Effort

- Build 30 test vehicles (sub-set of the 193 assembled)
 - Test vehicles will be built with Lead-Free solder and Lead-Free component finishes only = similar to Manufactured test vehicles for Mechanical Shock, Vibration and Drop Testing
 - Lead-Free alloys, SAC305 and SN100C
 - Rework will be done using only SnPb solder
 - Perform multiple pass rework 1 to 2 times on random Pb-free
 DIP, TQFP-144, TSOP-50, LCC and QFN components
 - Testing
 - Thermal Cycling -55°C to +125°C
 - Vibration Testing
 - Drop Testing

Thermal Cycle -20/+80°C

when the to Decision p 5 to 10°C/r 30 minute 1 10 minute	est is complete oint 10,000 cyc minute ramp high temperatur low temperature	les re dwell	e data and determine	
	İ	Rework		
Mfg. LF	Rwk. SnPb	Rwk. SnPb ENIG	Rwk. LF	
5 5		1	5	
	when the to Decision p to 10°C/r to 30 minute to 10 minute st Vehicles Reconctured	when the test is complete Decision point 10,000 cyc to 10°C/minute ramp 30 minute high temperature 10 minute low temperature st Vehicles Required	- Decision point 10,000 cycles - 5 to 10°C/minute ramp - 30 minute high temperature dwell - 10 minute low temperature dwell st Vehicles Required ctured Rework Mfg LF Rwk SnPh Rwk SnPb	





Phase 1 = JCAA/JGPP Lead Free Solder Project Test Results

• 27,135 thermal cycles

- All of the ceramic leadless chip carriers (CLCC's) and TSOP's had failed
- Most of the BGA's had failed (SnPb solder/SnPb balls; SAC solder/SAC balls; SACB solder/SAC balls; and mixed technologies)
- Most of the TQFP-144's had failed



Combine Environments Testing

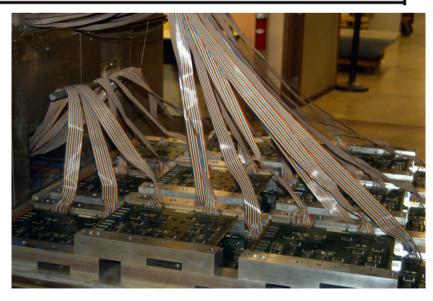
Parameters

- -55°C to +125°C
- Number of cycles ≥ 500
- 20°C/minute ramp
- 15 minute soak
- Vibration for duration of thermal cycle
- 10 G_{ms}, initial
- Increase 5 Grms after every 50 cycles
- 55 Gms, maximum

Number of Test Vehicles Required

42	Manu	ıfactured	Rework			
Mfg. SnPb	Mfg. LF	Mfg. LF SN100C	Mfg. LF ENIG	Rwk. SnPb	Rwk. SnPb ENIG	Rwk. LF
5	5	5	1	5	1	5
Trials per Specimens		1			**	

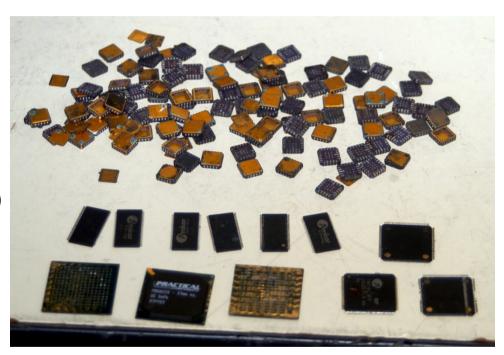




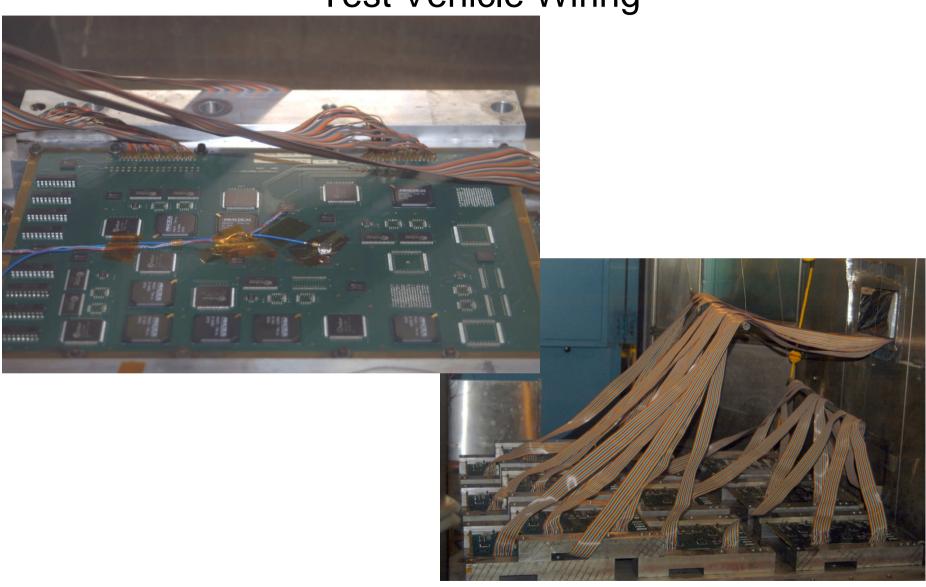
Raytheon

Combine Environments Testing Status

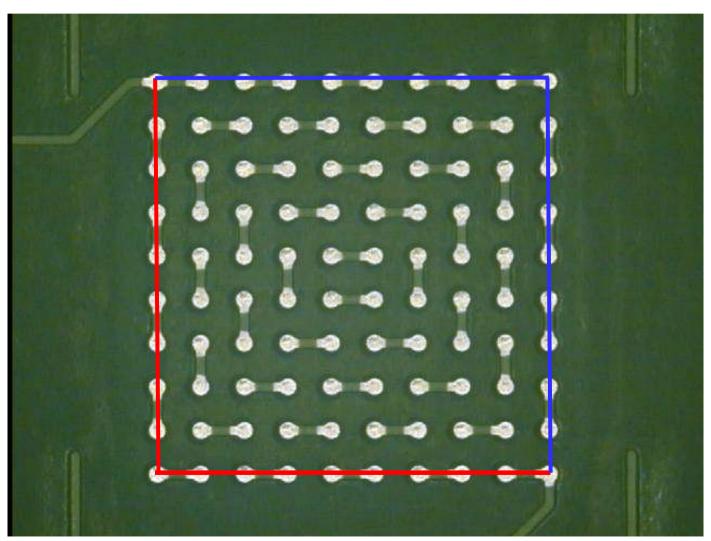
- Manufactured Test Vehicles
- 650 cycles completed on April 1, 2009
- Results
 - 121 of 150 BGA's failed (81%)
 - 139 of 150 CLCC's failed (93%)
 - 57 of 150 CSP's failed (38%)
 - 3 of 60 Sn PDIP's failed (5%)
 - 2 of 60 NiPdAu PDIP's failed (3%)
 - 20 of 75 QFN's failed (27%)
 - includes component U15
 - 44 of 150 TQFP's failed (29%)
 - 36 of 150 TSOP's failed (24%)



Combine Environments Testing Test Vehicle Wiring



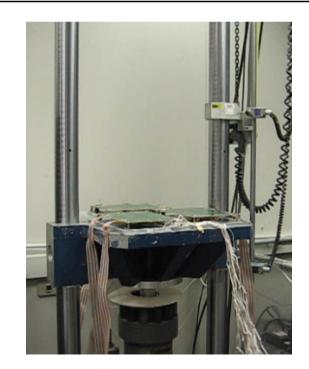
When reviewing the CSP data, please note that the CSP components on <u>all test vehicles</u> only have continuity in the outside solder balls.



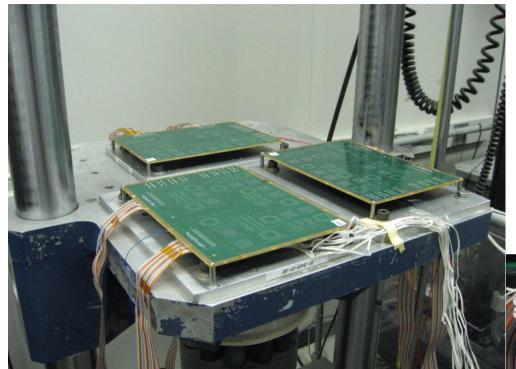
Drop Testing

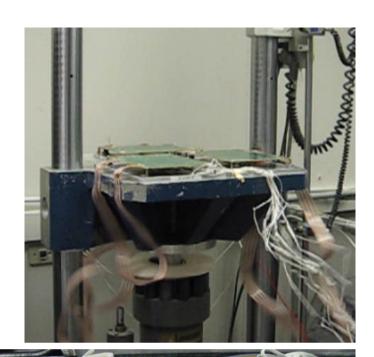
Parameters - Shock testing will be conducted in the -Z direction - 340Gpk input, 2ms pulse duration - Test vehicles will be dropped until all monitored components fa 10 drops have been completed									
Number of Te	st Vehicles F	kequired							
Manufa	ctured		Rework						
Mfg. SnPb	Mfg. LF	Rwk. SnPb	Rwk. SnPb ENIG	Rwk. LF					
5	5	5	1	5					
Trials per Spec	imen An	naximum of 10 dro	ps						





Drop Testing









Slide 24 5-Jun-09

 Perform multiple pass <u>SnPb</u> rework 1 to 2 times on random Pb-free DIP, TQFP-144, TSOP-50, LCC and QFN components

		Number of Reworks							
		Drop 1	est Board	l (Lead-Fr	ee Manufa	actured B	atch F)		
RefDes	Part	SN80	SN82	SN84	SN85	SN86	SN87		
U16	TSOP 50/SnBi	2	2	2	1	2	1		
U24	TSOP 50/SnBi	0	0	0	0	0	0		
U26	TSOP 50/SnBi	0	0	0	1	0	1		
U40	TSOP 50/SnBi	1	1	1	2	1	2		
U62	TSOP 50/SnBi	1	1	1	0	1	0		
U12	TSOP 50/Sn	0	0	0	0	0	0		
U25	TSOP 50/Sn	2	2	2	1	2	1		
U29	TSOP 50/Sn	1	1	1	2	1	2		
U39	TSOP 50/Sn	0	0	0	1	0	1		
U61	TSOP 50/Sn	1	1	1	0	1	0		
U9	CLCC-SAC305	2	2	2	1	2	1		
U10	CLCC-SAC305	1	1	1	2	1	2		
U13	CLCC-SAC305	0	0	0	0	0	0		
U14	CLCC-SAC305	0	0	0	0	0	0		
U17	CLCC-SAC305	2	2	2	1	2	1		
U22	CLCC-SAC305	1	1	1	2	1	2		
U45	CLCC-SAC305	0	0	0	1	0	1		
U46	CLCC-SAC305	1	1	1	0	1	0		
U52	CLCC-SAC305	0	0	0	1	0	1		
U53	CLCC-SAC305	1	1	1	0	1	0		
U1	TQFP-144/Sn	2	2	2	1	2	1		
U3	TQFP-144/Sn	0	0	0	0	0	0		
U7	TQFP-144/Sn	1	1	1	2	1	2		
U20	TQFP-144/Sn	0	0	0	0	0	0		
U31	TQFP-144/Sn	2	2	2	1	2	1		
U34	TQFP-144/Sn	1	1	1	2	1	2		
U41	TQFP-144/Sn	0	0	0	1	0	1		
U48	TQFP-144/Sn	1	1	1	0	1	0		
U57	TQFP-144/Sn	0	0	0	1	0	1		
U58	TQFP-144/Sn	1	1	1	0	1	0		
U08	PDIP-20/NiPdAu	1	1	1	2	1	2		
U49	PDIP-20/NiPdAu	1	1	1	2	1	2		
U23	PDIP-20/NiPdAu	1	1	1	0	1	0		
U30	PDIP-20/Sn	0	0	0	1	0	1		
U38	PDIP-20/Sn	1	1	1	0	1	0		
U11	PDIP-20/Sn	1	2	2	1	2	1		
U51	PDIP-20/Sn	1	2	2	1	2	1		
U59	PDIP-20/Sn	0	1	1	0	1	0		
U15	QFN/Sn	2	2	2	1	2	1		
U27	QFN/Sn	1	1	1	2	1	2		
U28	QFN/Sn	2	2	2	1	2	1		
U47	QFN/Sn	0	0	0	1	0	1		
U54	QFN/Sn	1	1	1	0	1	0		

- The test vehicles are LF Manufactured Batch F
 - LF Reflow (SAC305) / Wave (SN100C)
 - LF profiles
 - All BGA components have SAC405 balls.
- Perform multiple pass <u>SnPb</u> rework 1 to 2 times on random Pb-free DIP, TQFP-144, TSOP-50, LCC and QFN components
- Test vehicles 80, 82, 87 were subjected to 10 drops at 340G and then 10 drops at 500G
- Test vehicles 84, 85, 86; 83, 81, 60 were subjected to 20 drops at 500G only

	PBGA 22	5							
	82	80	87	86	85	84	83	81	60
U18	12	17	15	10	2	6	9	17	0
U56	14	11	13	7	9	8	16	7	14
U55	19	11	19	7	6	3	9	6	15
U2	4	11	14	4	6	4	5	15	17
U4	10	11	6	3	2	4	2	9	6
U43	11	11	6	3	5	6	7	5	8
U21	8	8	10	5	5	3	5	4	5
U44	13	12	10	10	9	7	12	11	16
U5	5	7	5	4	3	2	5	4	4
U6	7	7	5	4	2	2	5	3	3

	CABGA 1	100							
	82	80	87	86	85	84	83	81	60
U32	0	0	0	0	0	0	0	0	0
U50	0	0	0	0	0	0	0	0	0
U33	0	0	0	0	0	0	0	0	0
U36	0	0	0	0	0	0	0	0	0
U19	0	0	0	0	0	0	0	0	0
U42	0	0	0	0	0	0	0	0	0
U37	0	0	0	0	0	0	0	0	0
U35	0	0	0	0	0	0	0	0	0
U63	0	0	0	0	0	0	0	0	0
U60	0	0	0	0	0	0	0	0	0

	CLCC 20									
	82	80	87	86	85	84	83	81	60	
U9	0	0	0	0	0	0	0	0	0	
U13	0	0	0	0	0	0	0	0	0	
U14	0	0	0	0	0	3	0	0	0	0 Rework
U17	0	0	0	0	0	0	0	0	0	
U45	0	0	0	0	0	0	0	0	0	
U46	0	0	0	0	0	0	0	0	0	
U22	0	0	0	0	0	0	0	0	0	
U52	0	0	0	0	0	0	0	0	0	
U53	0	0	0	0	0	0	0	0	0	
U10	0	0	0	0	0	0	0	0	0	

			_							
	QFN 20									
	82	80	87	86	85	84	83	81	60	
U27	0	0	0	0	0	0	0	0	0	
U15	0	0	0	18	0	0	0	0	0	2x Rework
U47	0	0	0	0	0	0	0	0	0	\Box
U54	0	0	0	0	0	0	0	0	0	
U28	0	0	0	0	0	0	0	0	0	
	PDIP 20									1
	82	80	87	86	85	84	83	81	60	
U11	0	0	0	0	0	0	0	0	0	
U30	0	0	0	0	0	0	0	0	0	
U38	0	0	0	0	0	0	0	0	0	
U49	0	0	0	0	0	0	0	0	0	
U51	0	0	0	0	0	0	0	0	0	
U59	0	0	0	0	0	0	0	0	0	
U8	0	0	0	0	17	0	0	0	0	2x Rework
U23	0	0	0	0	0	0	0	0	0	Slide 28

	TQFP 14	4								
	82	80	87	86	85	84	83	81	60	
U1	0	0	0	0	0	0	0	0	0	
U41	0	0	0	0	0	0	0	0	0	
U3	0	0	0	0	0	0	0	0	0	
U57	0	0	0	0	7	0	0	0	0	1x Rework
U58	0	0	0	0	0	0	0	0	0	Γ
U31	0	0	0	0	0	0	0	0	0	
U20	0	0	0	0	0	0	0	0	0	
U48	0	0	0	0	0	0	0	0	0	
U7	0	0	0	0	0	0	0	0	0	
U34	0	0	0	0	0	0	0	0	0	

	TSOP 50								
	82	80	87	86	85	84	83	81	60
U26	0	0	0	0	0	0	0	0	0
U39	0	0	0	0	0	0	0	0	0
U40	0	0	0	0	0	0	0	0	0
U25	0	0	0	0	0	0	0	0	0
U12	0	0	0	0	0	0	0	0	0
U24	0	0	0	0	0	0	0	0	0
U61	0	0	0	0	0	0	0	0	0
U16	0	0	0	0	0	0	0	0	0
U62	0	0	0	0	0	0	0	0	0
U29	0	0	0	0	0	0	0	0	0

Thermal Cycle -55/+125°C

Parameter	- Cyc whe - Dec - 5 to - 30 r - 10 r	to +125°C les: The proje in the test is c ision point at 10°C/minute ninute high te ninute low ter icles Require	omplete 2,000 and 4, ramp mperature dy mperature dy	000 cycles well	the data and dete	
		factured			Rework	
Mfg. SnPb	Mfg. LF	Mfg. LF SN100C	Mfg. LF ENIG	Rwk. SnPb	Rwk. SnPb ENIG	Rwk. LF
5	5	5	1	5	1	5
Trials per	Specimen	1	30 30		22	





Slide 30 5-Jun-09

Vibration

Paramete	p is h	erpendicular s completed. our at 28.0 g	to the plane Vibrate for 1	of the test vehi	ements in the ax cles until the 20 est level. Finish	.0 g _{rms} level
Number	of Test V	ehicles Requ	uired			
	Mar	nufactured			Rework	
Mfg. SnPb	Mfg. LF	Mfg. LF ENIG	Mfg. LF SN100C	Rwk. SnPb	Rwk. SnPb ENIG	Rwk. LF
5	5	1	5	5	1	5
Trials per	Specime	n	1		.000	III





Mechanical Shock

Parameters	The shock transients will be applied perpendicular to the plane of the board and will be increased after every 100 shocks (i.e., a step stress							
		test). Frequency range is 40 to 1000 Hz. SRS damping: 5%						
	Test S	Test Shock Response Spectra			itude	Te	Shocks per	
				(G	's)	(msec)	Level	
		Modified Functional Test for Flight Equipment (Level 1)			0	<30	100	
		Modified Functional Test for Ground Equipment (Level 2)			0	<30	100	
		fied Crash Haza nd Equipment (I		7:	5	<30	100	
	Level	4		10	00	<30	100	
	Level	.5		20	00	<30	100	
	Level	. 6		30	00	<30	100	
	Level	.7		50	00	<30	100	
	Level	. 8		70	00	<30	100	
Number of	Test V	ehicles Require	d					
Ma	nufact	ured		Rework				
Mfg. SnP	Mfg. SnPb Mfg. LF		Rwk. SnPb			k. SnPb ENIG	Rwk. LF	
5 5		5		1		5		
Trials per Sp	ecime	n	1					





Interconnect Stress Test (IST)

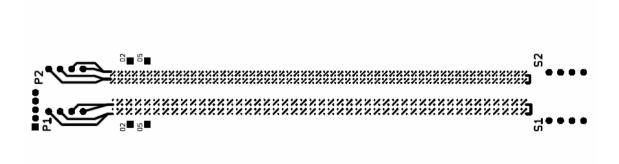
 Accelerates thermal cycling testing by heating a specifically designed test coupon to 150°C (higher temperatures in specific applications in exactly 3 minutes followed by cooling to ambient in approximately two minutes.

 Assembly and rework simulation is achieved by subjecting the coupon to heating to 230°C (260°C for lead-free applications) in three minutes followed by cooling to

ambient in approximately 2 minutes.

Three thermal cycles simulate assembly

Six thermal cycles simulate assembly and rework

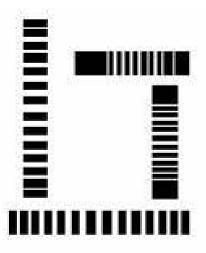






Copper Dissolution

Parameter	S	●As assem	onent remov off coupon as	als: 1X versus 3X nd QFP pad patter hickness :hickness			
Number of	Test Veh	icles Break off Co	upons Requi	red			
	Manufact	lured		Rewor	ık.		
Mfg. SnPb	Mfg. LF	Mfg. LF SN100C	Rwk. SnPb	Rwk. SnPb ENIG	Rwk. LF		
5	5	วิ	5 5 1 5				









NASA-DoD Lead-Free Electronics Project

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Website: www.teerm.nasa.gov

Web Links:

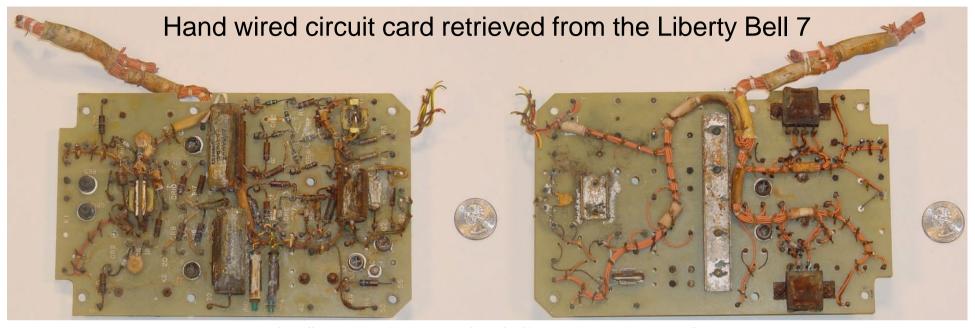
NASA-DoD Lead-Free Electronics Project:

http://www.teerm.nasa.gov/projects/NASA_DODLeadFreeElectronics_Proj2.html

• JCAA/JGPP Lead-Free Solder Testing for High Reliability:

http://www.teerm.nasa.gov/projects/LeadFreeSolderTestingForHighReliability_Proj1.html

Questions



(http://apollotribute.blogspot.com/2005/11/liberty-bell-7-circuit-card.html)



The Liberty Bell 7 was pulled from a depth of 15,000 feet -- 3,000 feet deeper than the Titanic



Slide 36 5-Jun-09